

AMENDMENTS TO THE CLAIMS:

Please amend Claims 1 and 10 as follows:

1. (Currently Amended) An image pickup apparatus comprising:

image pickup means for outputting an image signal having a first number of pixels which is greater than a predetermined number of pixels;

converting means for converting a moving image signal having the first number of pixels, outputted from said image pickup means, into an image signal having the predetermined number of pixels;

a first memory having storage capacity corresponding to the predetermined number of pixels, for storing the moving image signal having the predetermined number of pixels, outputted from said converting means;

a second memory having storage capacity corresponding to the first number of pixels; and

a memory interface arranged to generate addresses of said second memory, to write into said second memory a still image signal of the first number of pixels outputted from said image pickup means, and to read out a still image signal of the first number of pixels from said second memory, said memory interface being capable of generating addresses of a memory of larger capacity than said second memory;

still image processing means for processing the still image signal having the first number of pixels, read out by said memory interface; and

recording means for recording the moving image signal read out from said first memory on a recording medium in accordance with a predetermined recording format,

wherein the predetermined number of pixels conforms to the predetermined recording format, and

wherein said converting means, said first memory, said memory interface, and said still image processing means are provided on a single integrated circuit, and said image pickup means and said second memory are built as a circuit different from said single integrated circuit.

2. (Cancelled)

3. (Previously Presented) An apparatus according to claim 1, further comprising special effect means for performing a special effect processing on the image signal having the predetermined number of pixels, stored in said first memory, and outputting the thus-processed image signal, wherein said special effect means is provided on said single integrated circuit.

4. (Previously Presented) An apparatus according to claim 1, further comprising output means for converting the image signal having the predetermined number of pixels, stored in said first memory, into a predetermined format and outputting the thus-converted image signal, wherein said output means is provided on said single integrated circuit.

5. (Cancelled)

6. (Original) An apparatus according to claim 1, wherein said still image processing means includes encoding means for encoding the image signal outputted from said second memory and compressing an amount of information of the image signal.

7. (Original) An apparatus according to claim 1, wherein said still image processing apparatus includes a moving image processing mode and a still image processing mode, wherein in the moving image processing mode, said image pickup means adds together signals of pixels of vertically-adjacent lines of image pickup elements and reads out the added signals, and in the still image processing mode, said image pickup means reads out the signal of each line of the image pickup elements independently.

8. (Previously Presented) An apparatus according to claim 1, further comprising:  
moving image processing means for reading out from said first memory an image signal having the predetermined number of pixels and outputting the read-out image signal as moving image data;

first recording means for recording to a first recording medium the moving image data outputted from said moving image processing means; and

second recording means for recording to a second recording medium, which is different from the first recording medium, the still image data outputted from said still image processing means.

9. (Original) An apparatus according to claim 8, wherein the first recording medium is a magnetic tape, and the second recording medium is a memory card.

10. (Currently Amended) An image processing apparatus comprising:  
an input unit arranged to input an image signal having a first number of pixels which is greater than a predetermined number of pixels, said input unit being capable of inputting image signals of different number of pixels;

a conversion circuit arranged to convert a moving image signal having the first number of pixels, inputted from said input unit, into an image signal having the predetermined number of pixels;

a first memory having storage capacity sufficient for the predetermined number of pixels, arranged to store the moving image signal having the predetermined number of pixels, outputted from said conversion circuit;

a second memory having a storage capacity corresponding to the first number of pixels;

a memory interface arranged to generate addresses of said second memory, to write into said second memory a still image signal having the first number of pixels, outputted from said input unit, and to read out from said second memory a still image signal having the first number of pixels, said memory interface being capable of generating addresses of a memory of larger capacity than said second memory; and

a still image processing circuit arranged to process the still image signal having the first number of pixels, read out by said memory interface; and

a recording unit arranged to record the moving image signal read out from said first memory on a recording medium in accordance with a predetermined recording format,

wherein the predetermined number of pixels conforms to the predetermined recording format, and

wherein said input unit, said conversion circuit, said first memory, said memory interface, and said still image processing circuit are provided on a single integrated circuit, and said second memory is built as a circuit different from said integrated circuit.

11 - 14. (Cancelled)